



Docket No.: 252067US2S

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COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

RE: Application Serial No.: 10/827,331

Applicants: Hiroyuki KANAYA, et al.

Filing Date: April 20, 2004

For: SEMICONDUCTOR DEVICE AND ITS
MANUFACTURING METHOD

Group Art Unit: 2815

Examiner: Landau, M.

SIR:

Attached hereto for filing are the following papers:

ELECTION

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

HIROYUKI KANAYA ET AL

: EXAMINER: LANDAU, M.

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:

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ELECTION

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SIR:

In response to the election requirement dated May 12, 2005, Applicants elect Group I, Claims 1-17, drawn to a semiconductor device, classified in class 257, subclass 295, and further elect Species I, identified in the Official Action as shown in Figure 1, for further examination on the merits. Applicants identify Claims 1, 2, 5 and 8-12 as readable on the elected species. Applicants reserve the right to file one or more divisional applications directed to the non-elected species.

Accordingly, examination on the merits of Claims 1, 2, 5 and 8-12 is in order and is respectfully requested.

Respectfully submitted,

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